

FIG. 1A

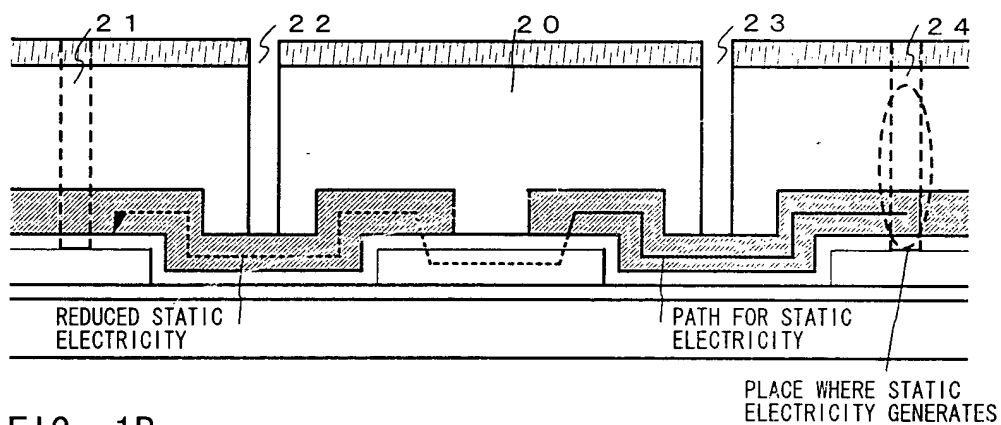


FIG. 1B

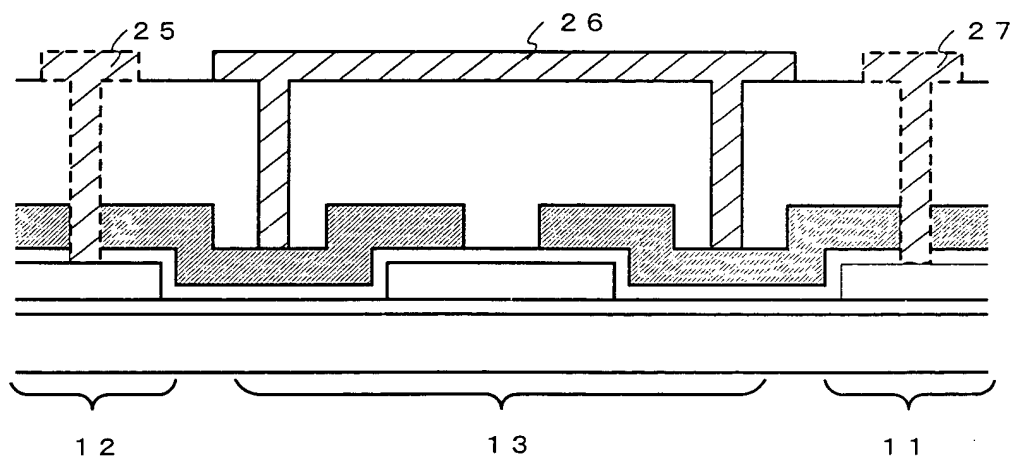


FIG. 1C

1005043-244

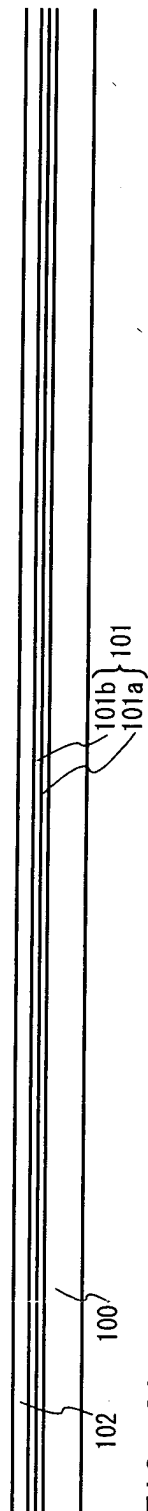


FIG. 2A

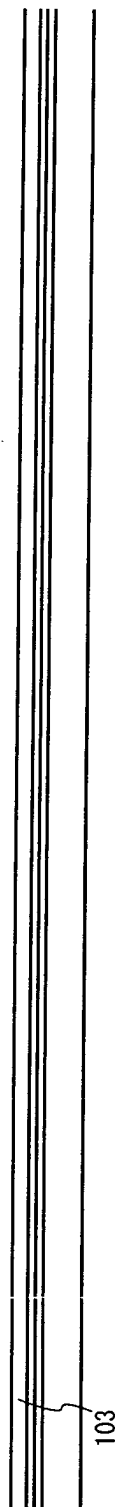


FIG. 2B

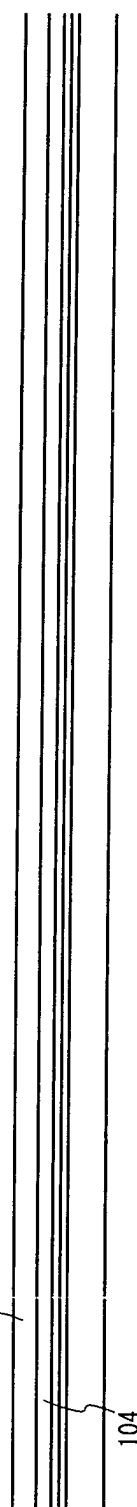


FIG. 2C

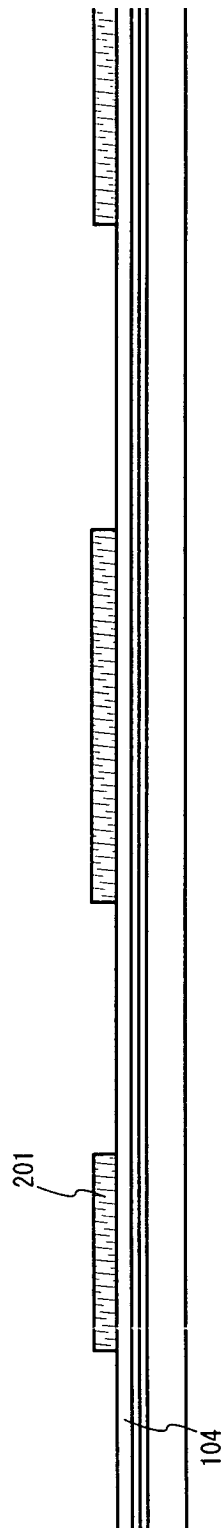


FIG. 3A

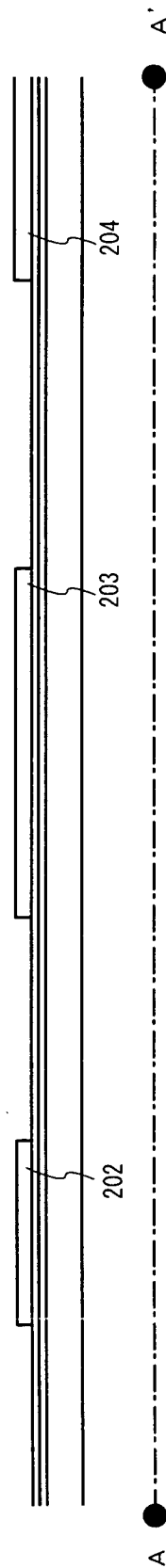


FIG. 3B

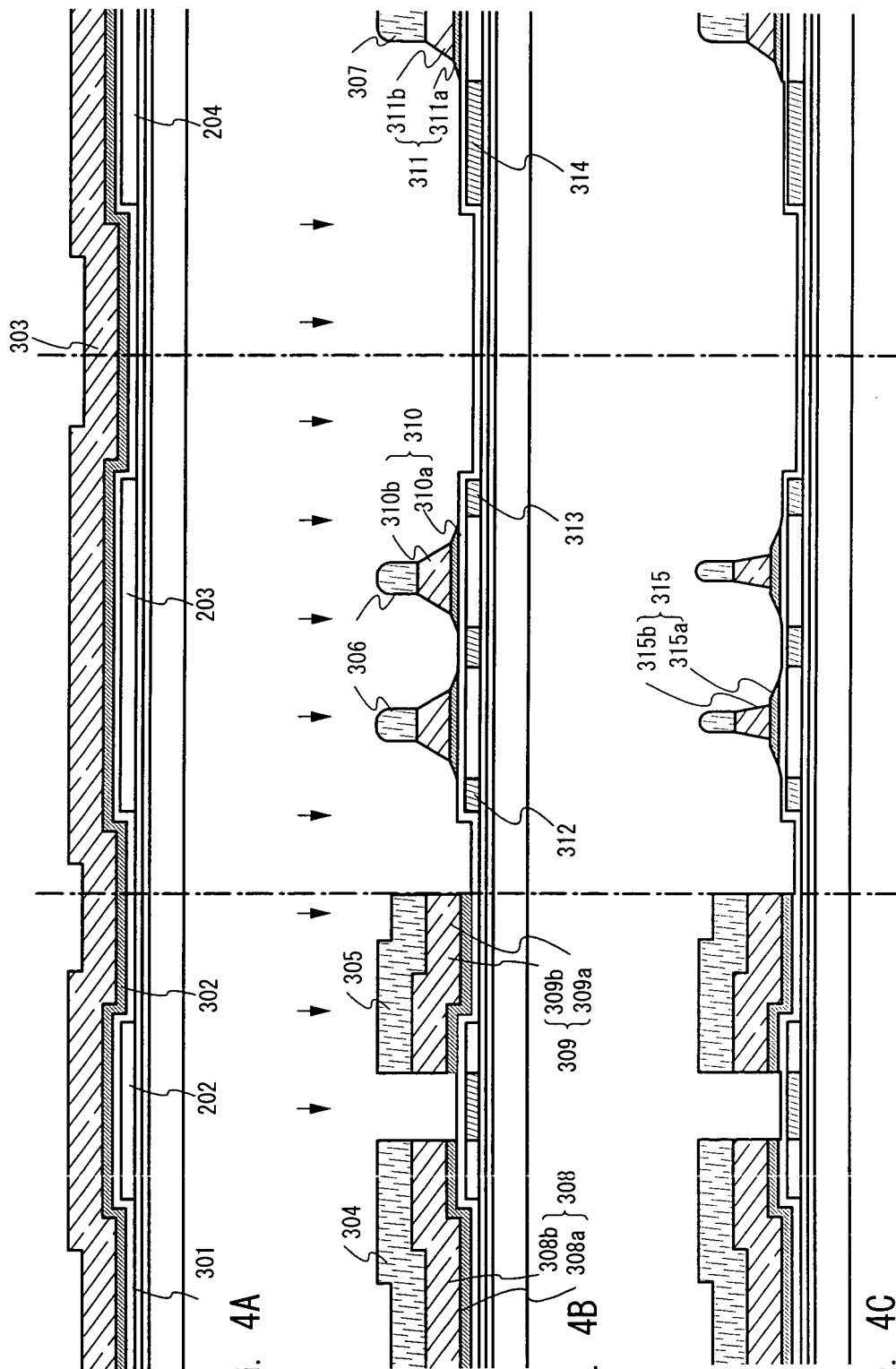


FIG. 4A

FIG. 4B

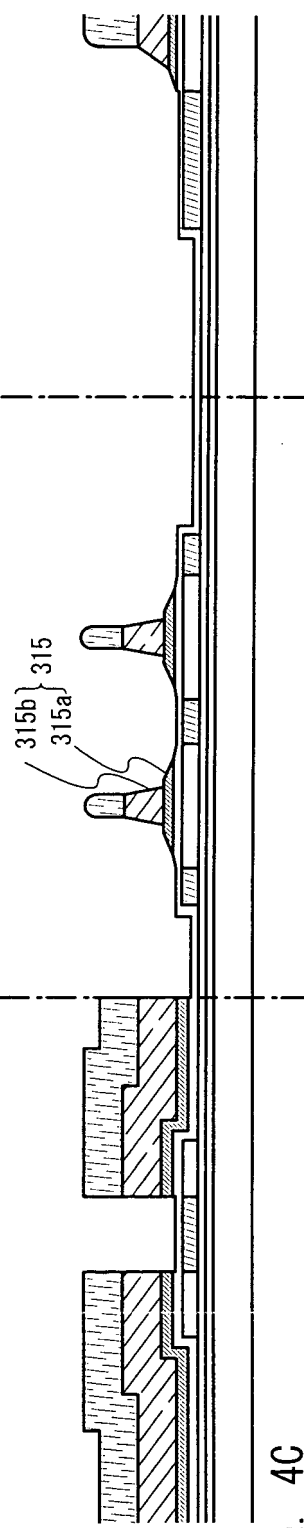


FIG. 4C

A ——— A'

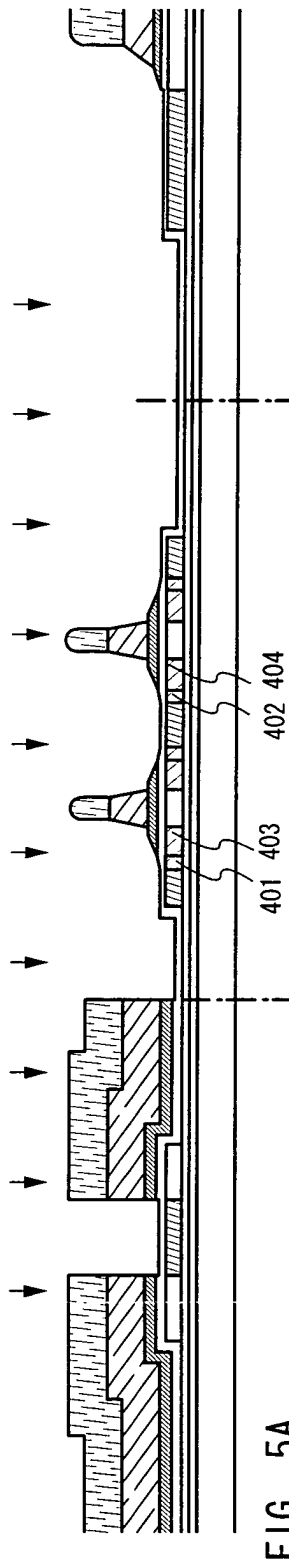


FIG. 5A

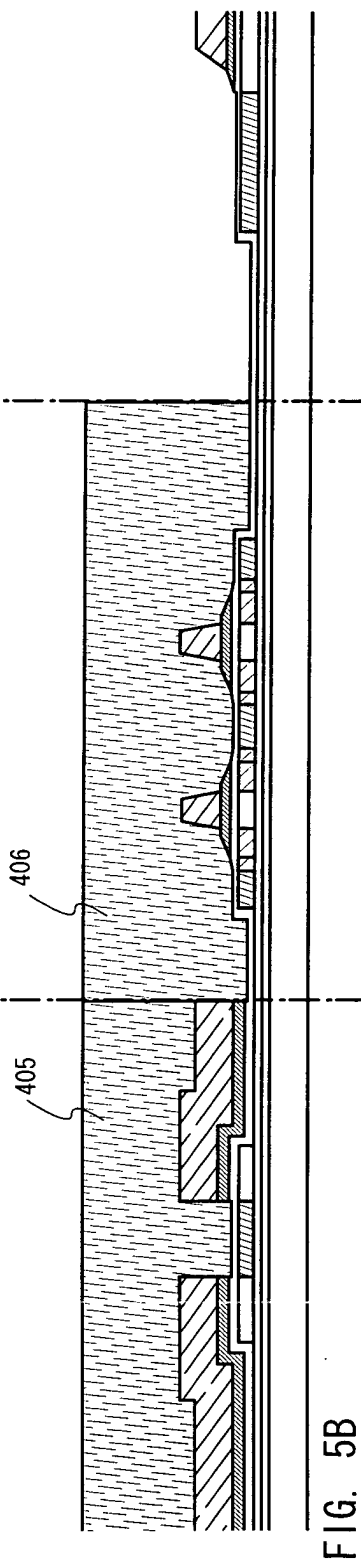


FIG. 5B

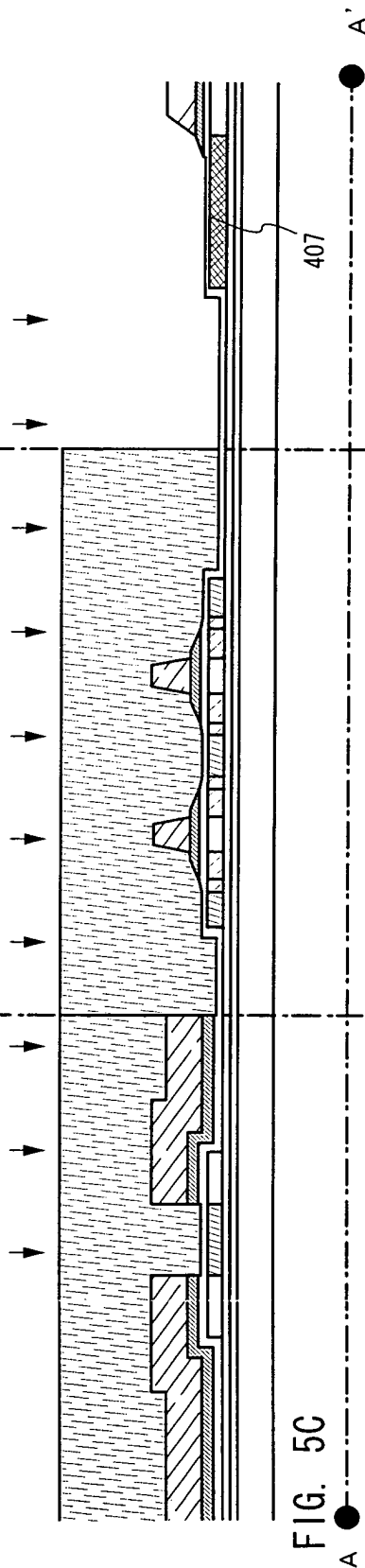


FIG. 5C

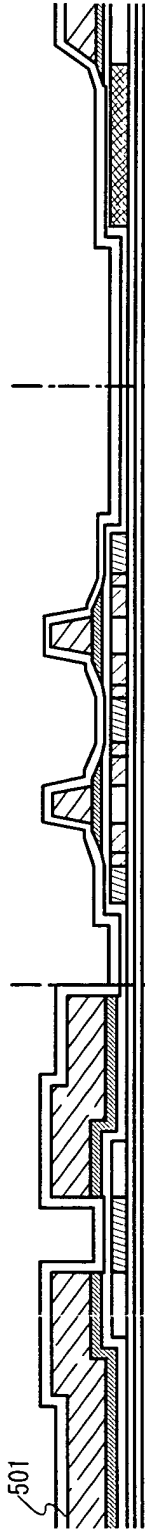


FIG. 6A

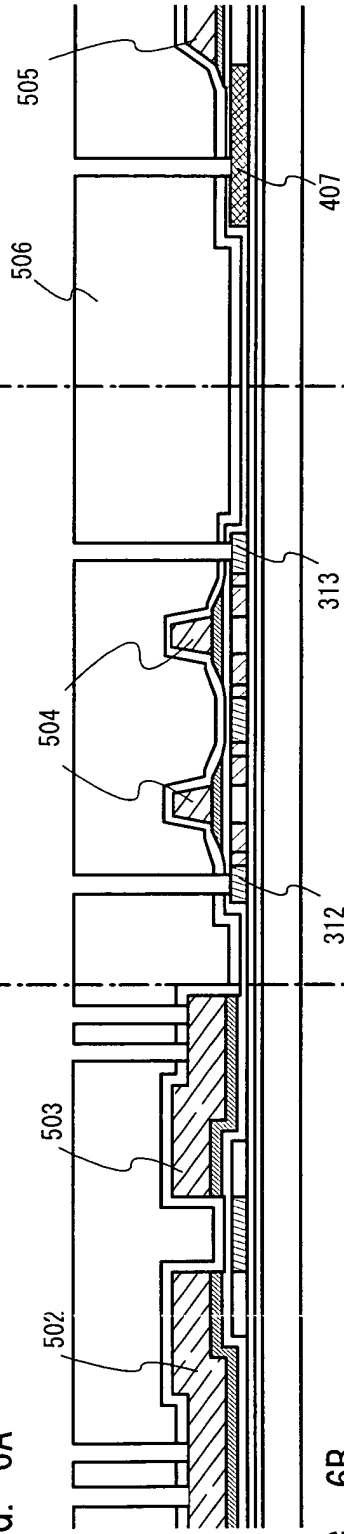


FIG. 6B

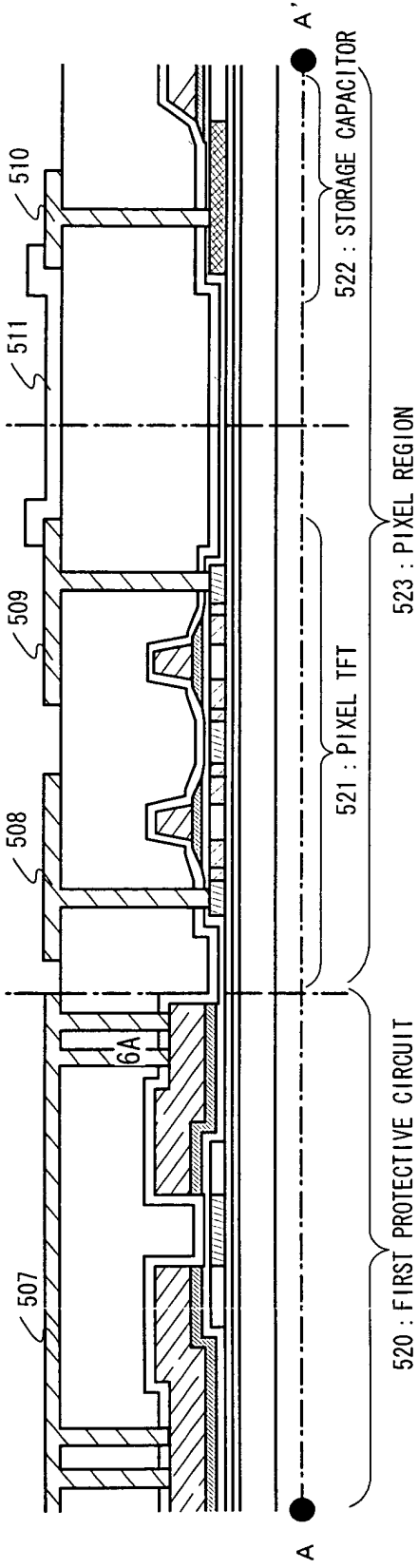


FIG. 6C

FIG. 7

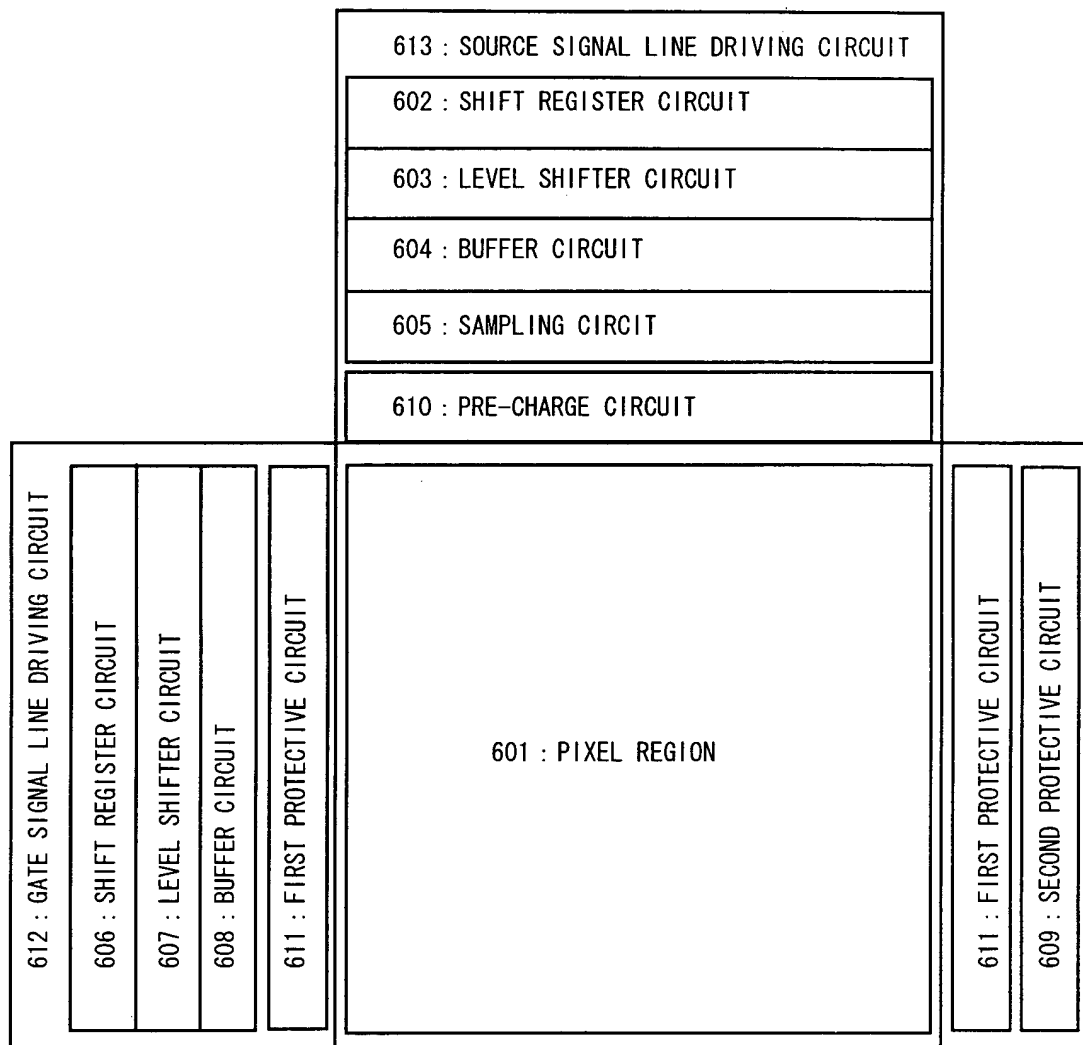


FIG. 7

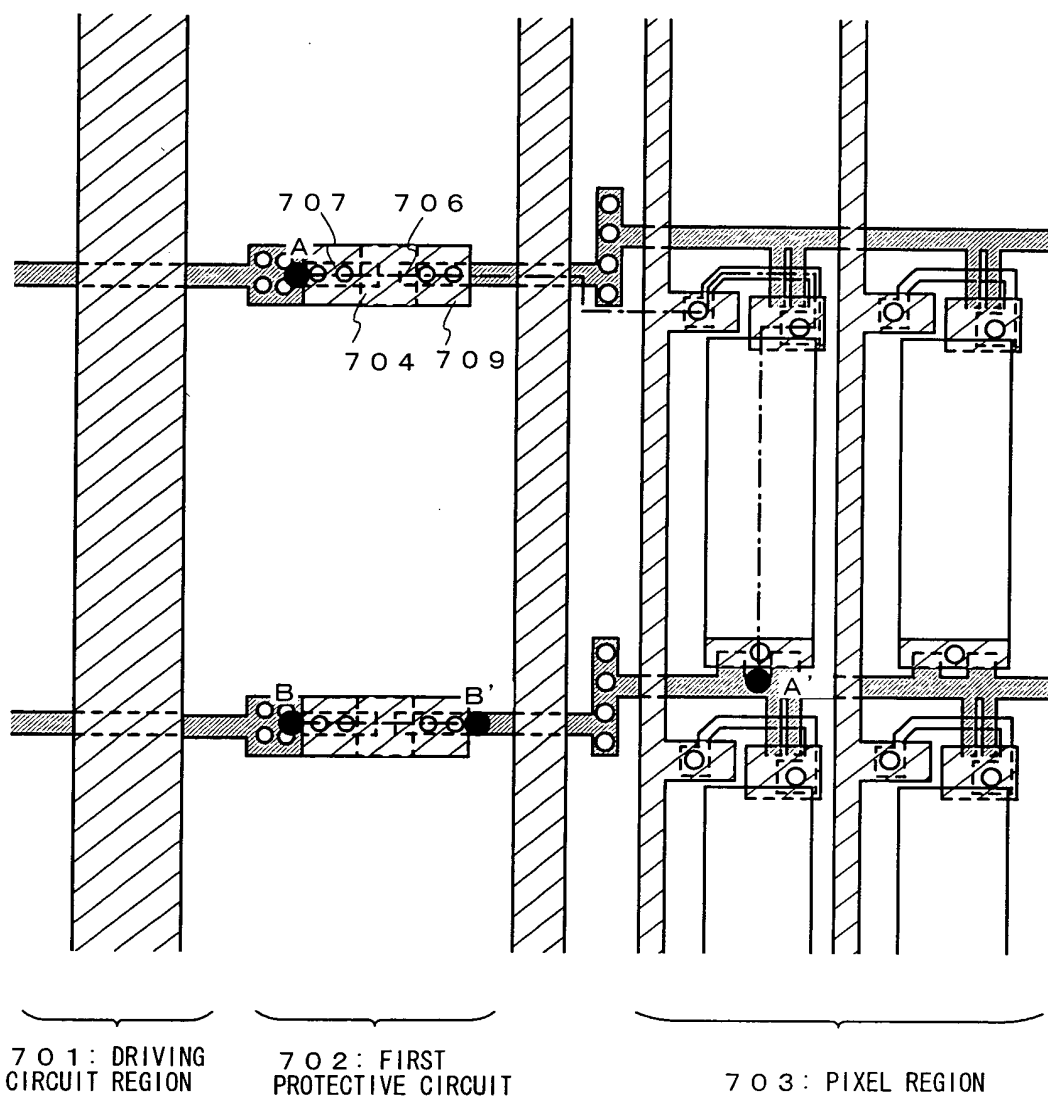


FIG. 8A

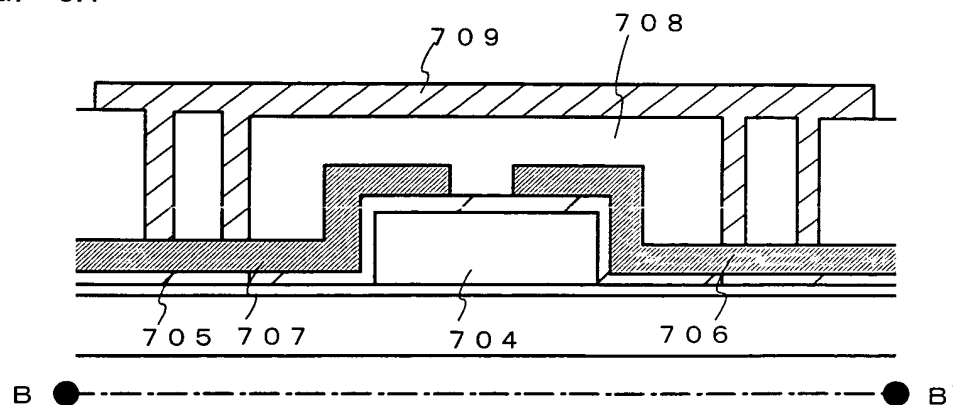


FIG. 8B

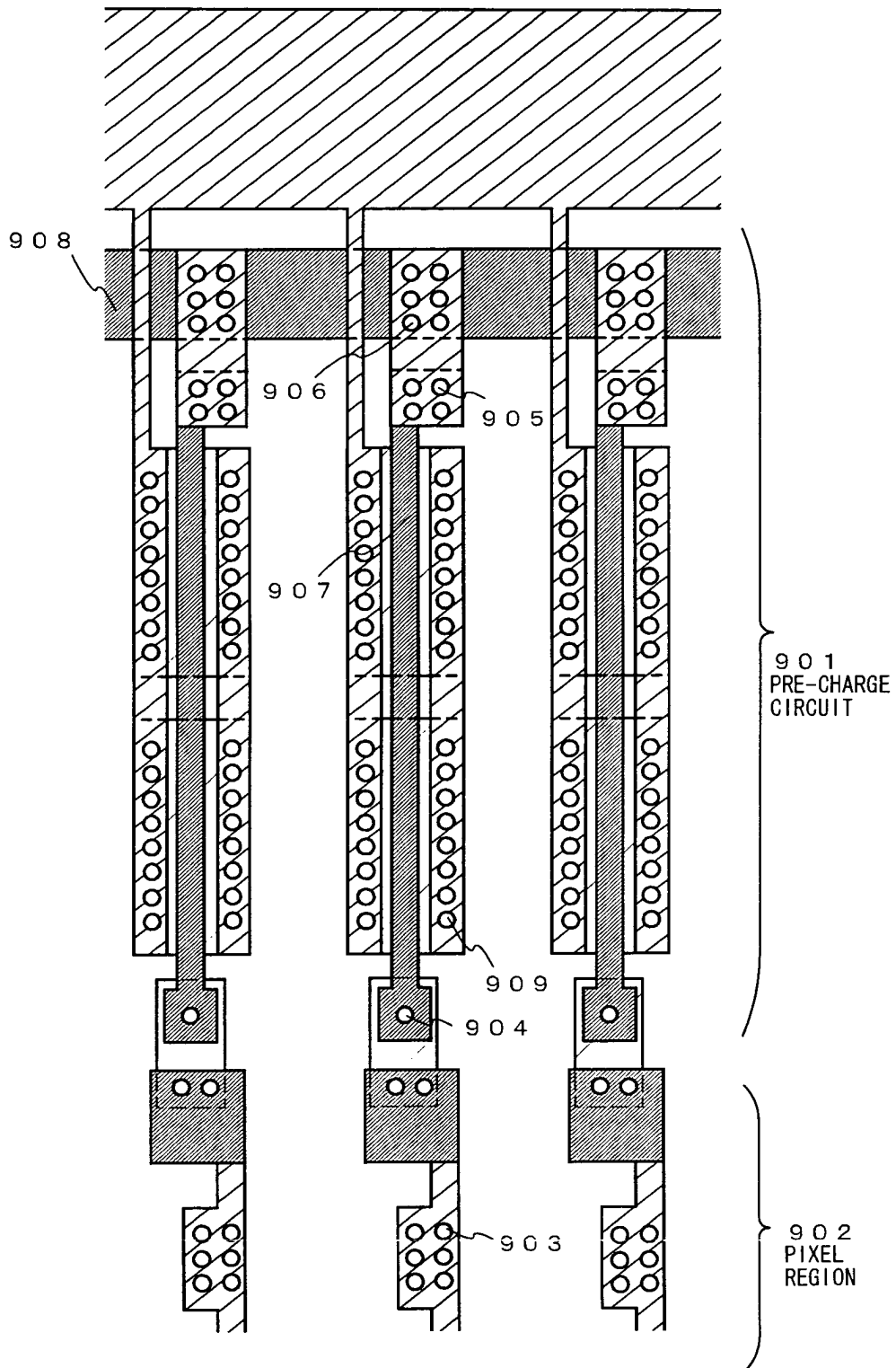


FIG. 10

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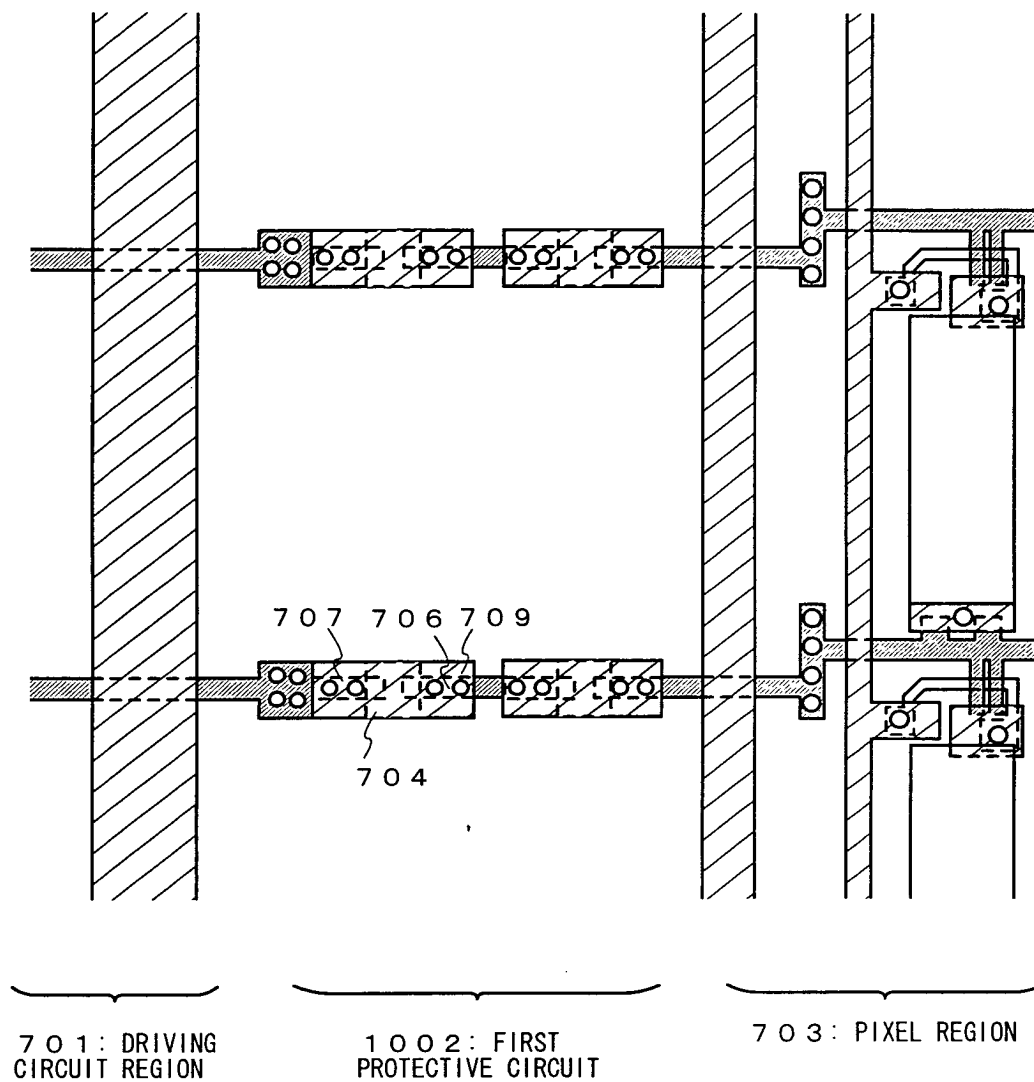


FIG. 11

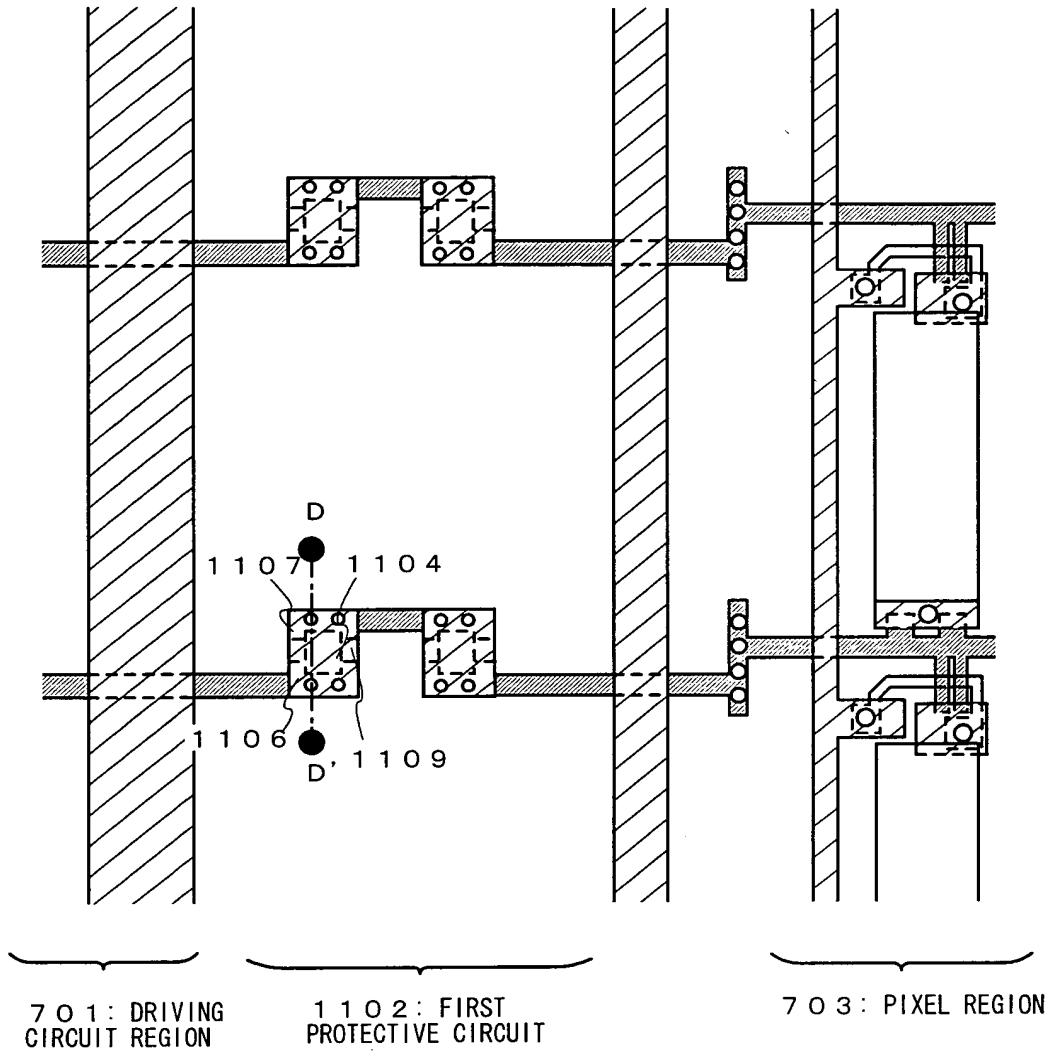


FIG. 12A

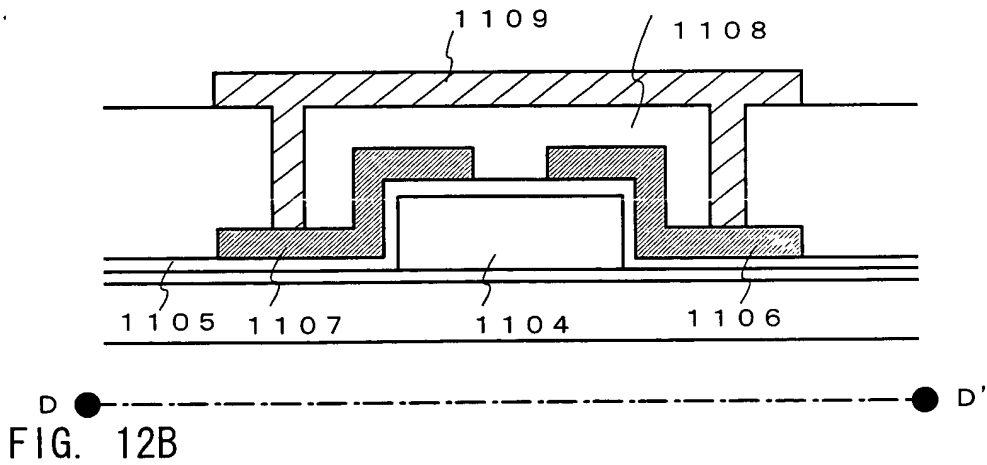
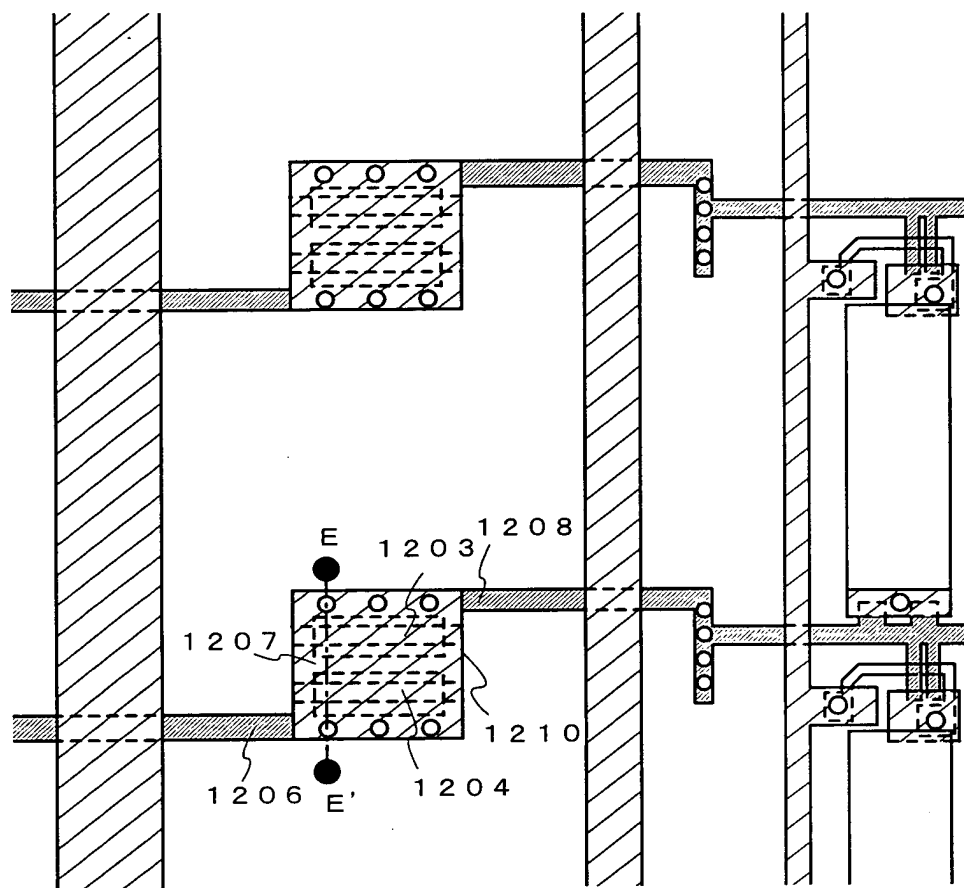


FIG. 12B

10065043 120401

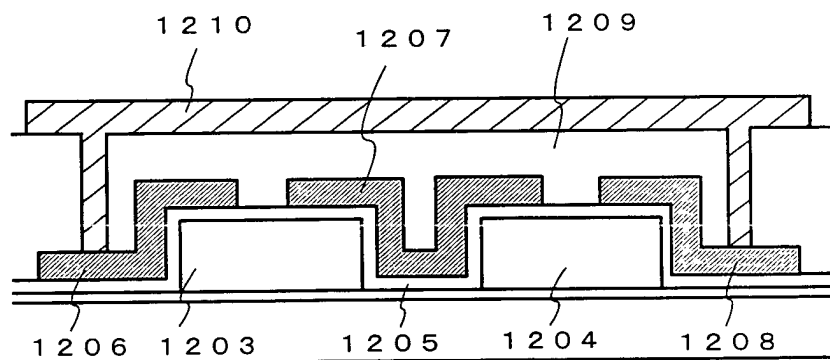


701: DRIVING CIRCUIT
REGION

1202: FIRST PROTECTIVE
CIRCUIT

703: PIXEL REGION

FIG. 13A



E ——— E'

FIG. 13B

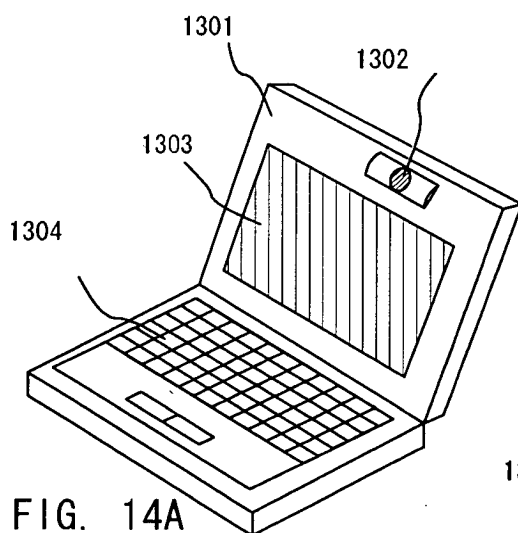


FIG. 14A

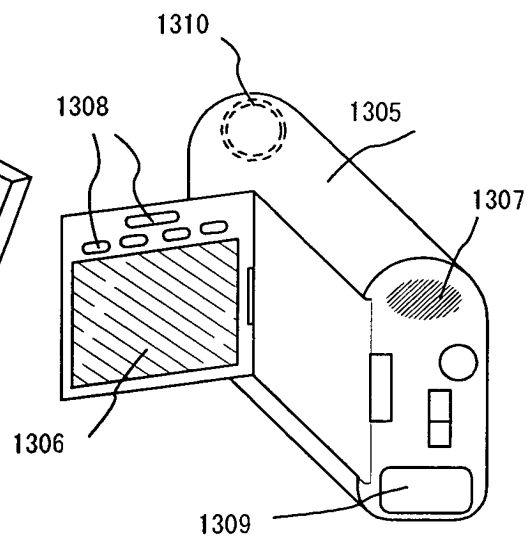


FIG. 14B

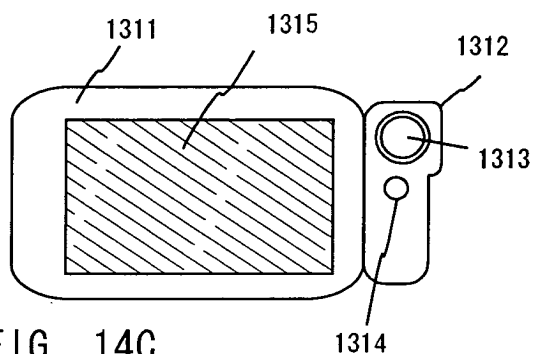


FIG. 14C

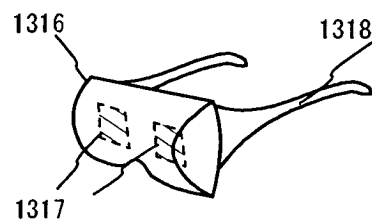


FIG. 14D

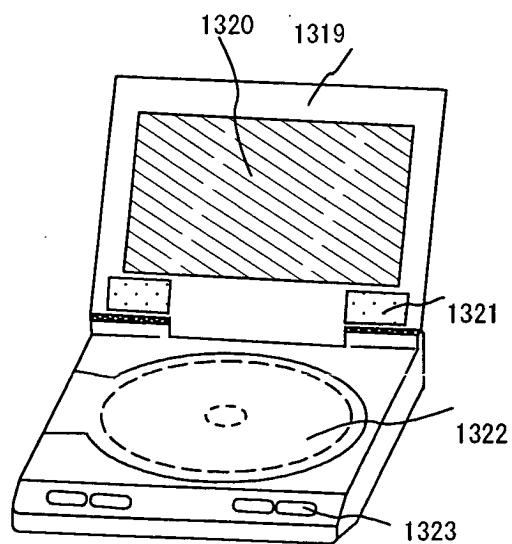


FIG. 14E

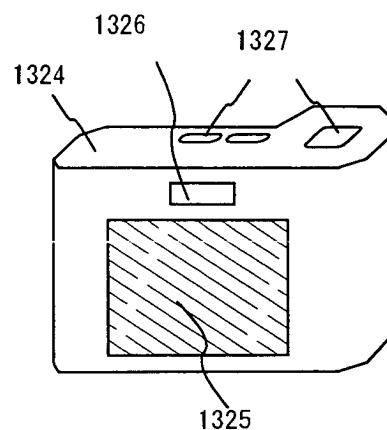


FIG. 14F

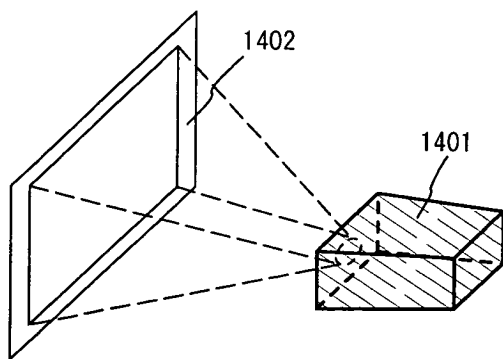


FIG. 15A

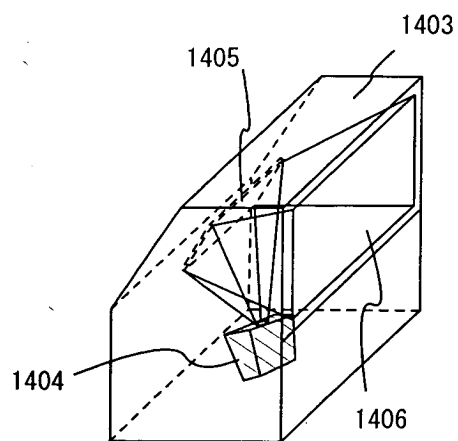


FIG. 15B

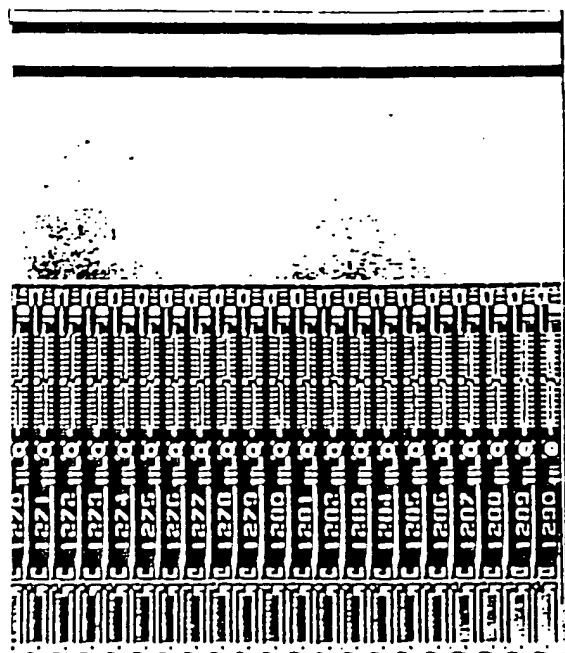
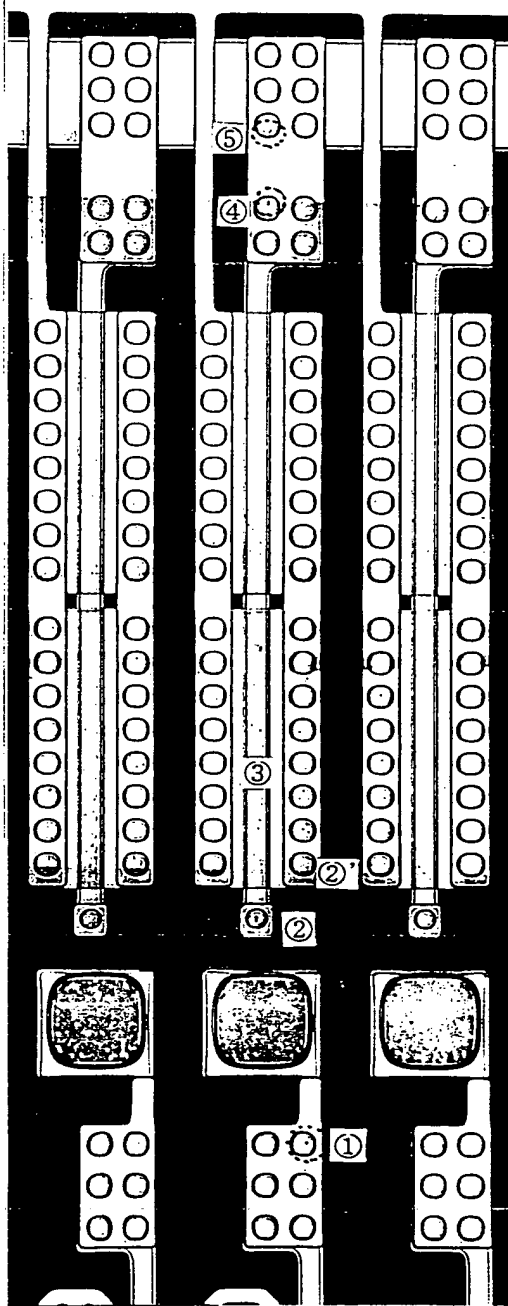


FIG. 16A



1279-th 1280-th 1281-th

FIG. 16B

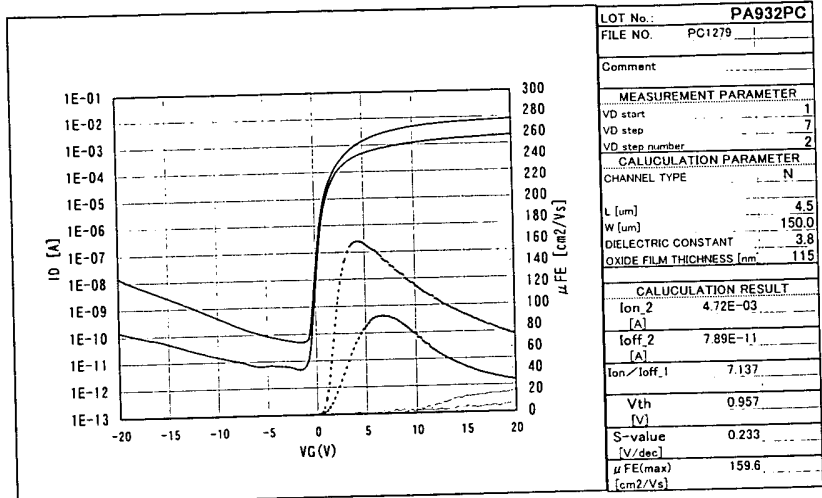


FIG. 17A

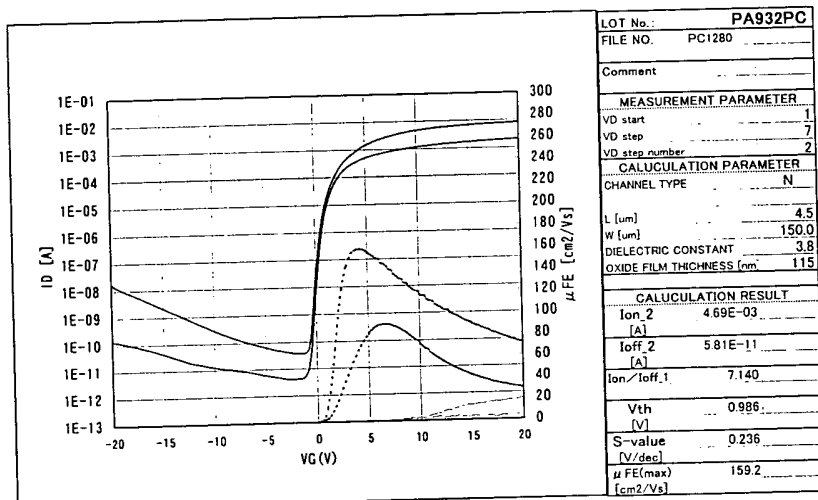


FIG. 17B

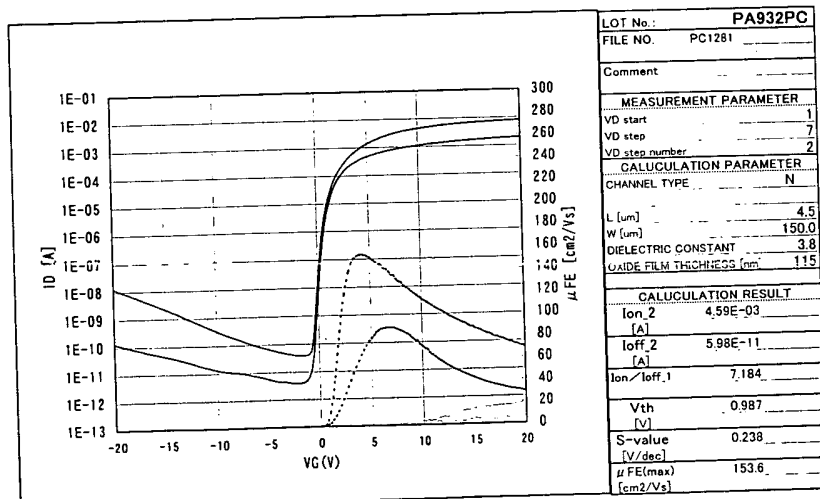


FIG. 17C

Pixel Region

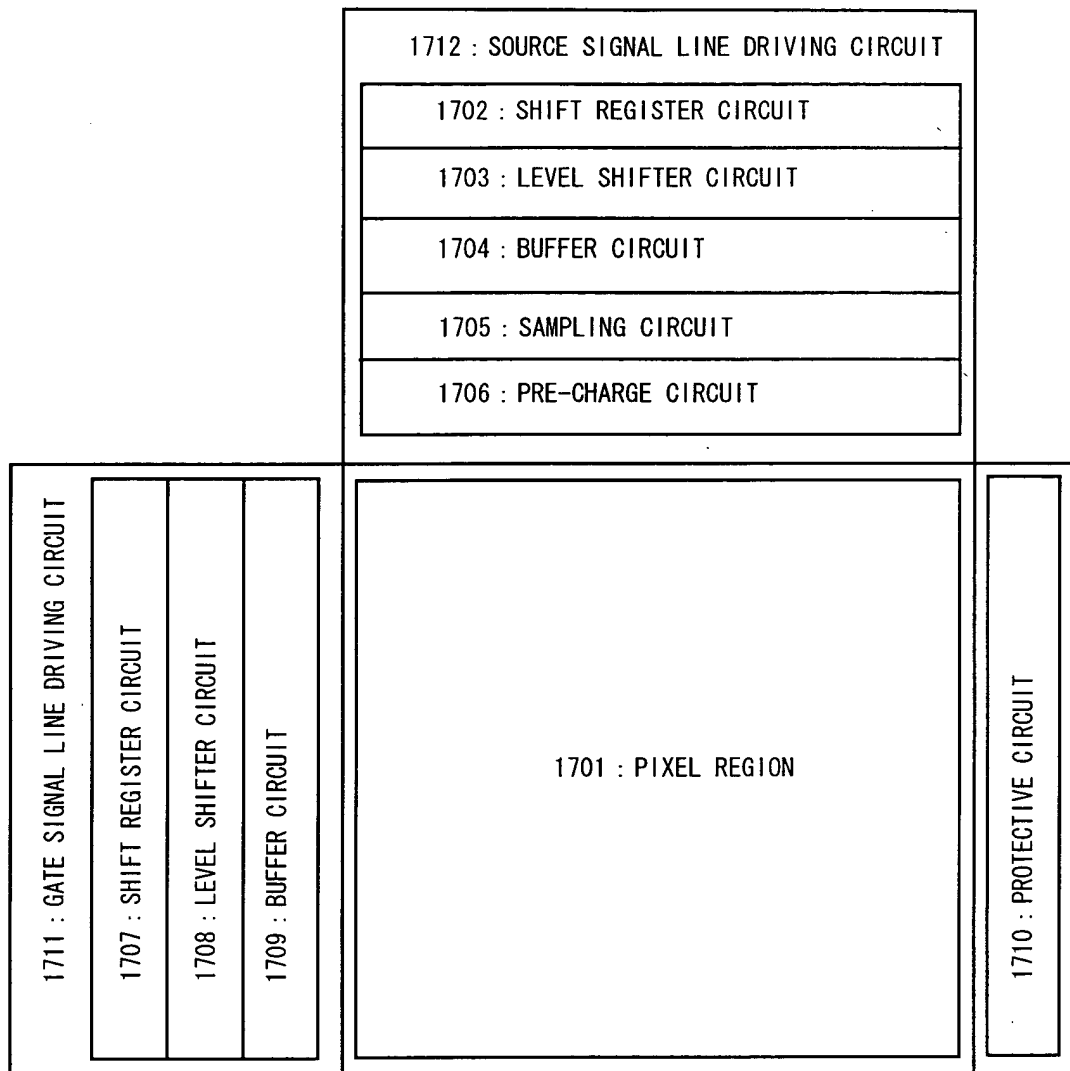


FIG. 18

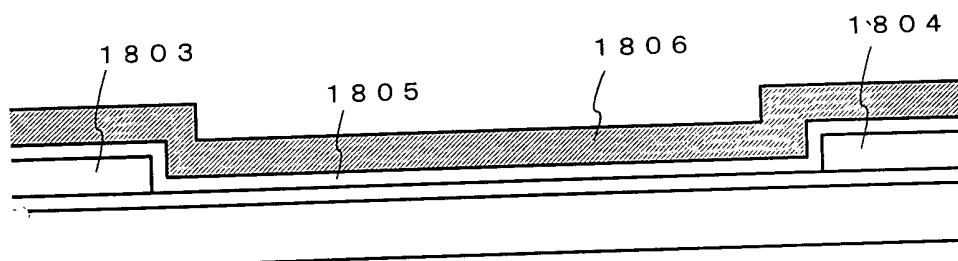


FIG. 19A

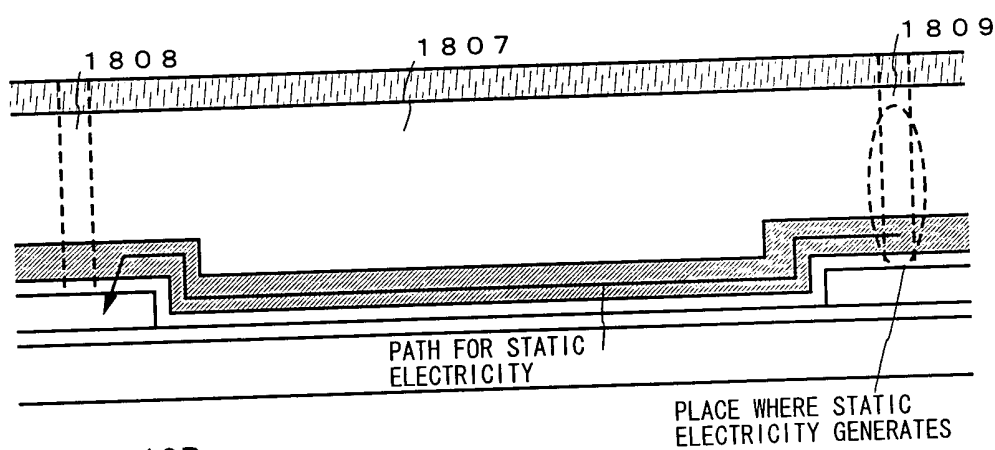


FIG. 19B

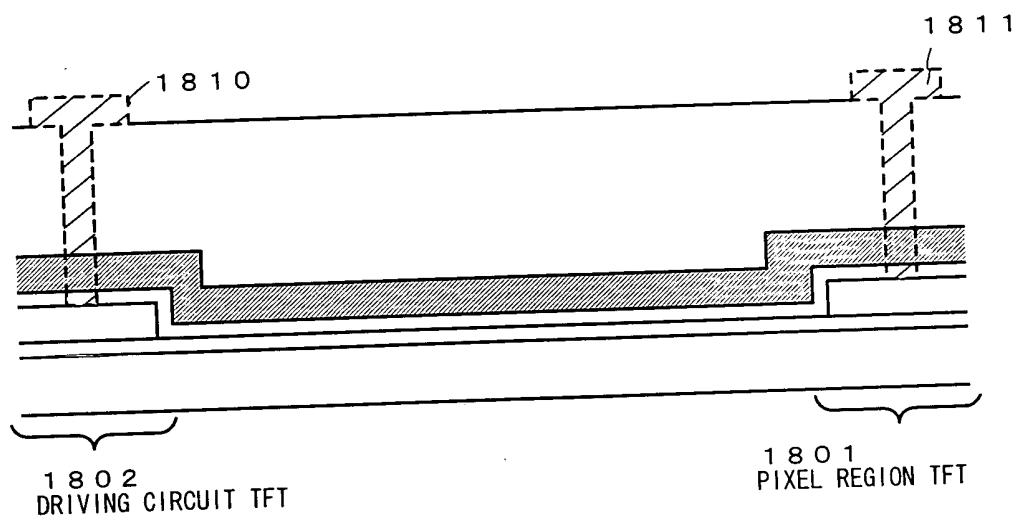


FIG. 19C

1005043 100403

10005043-120401

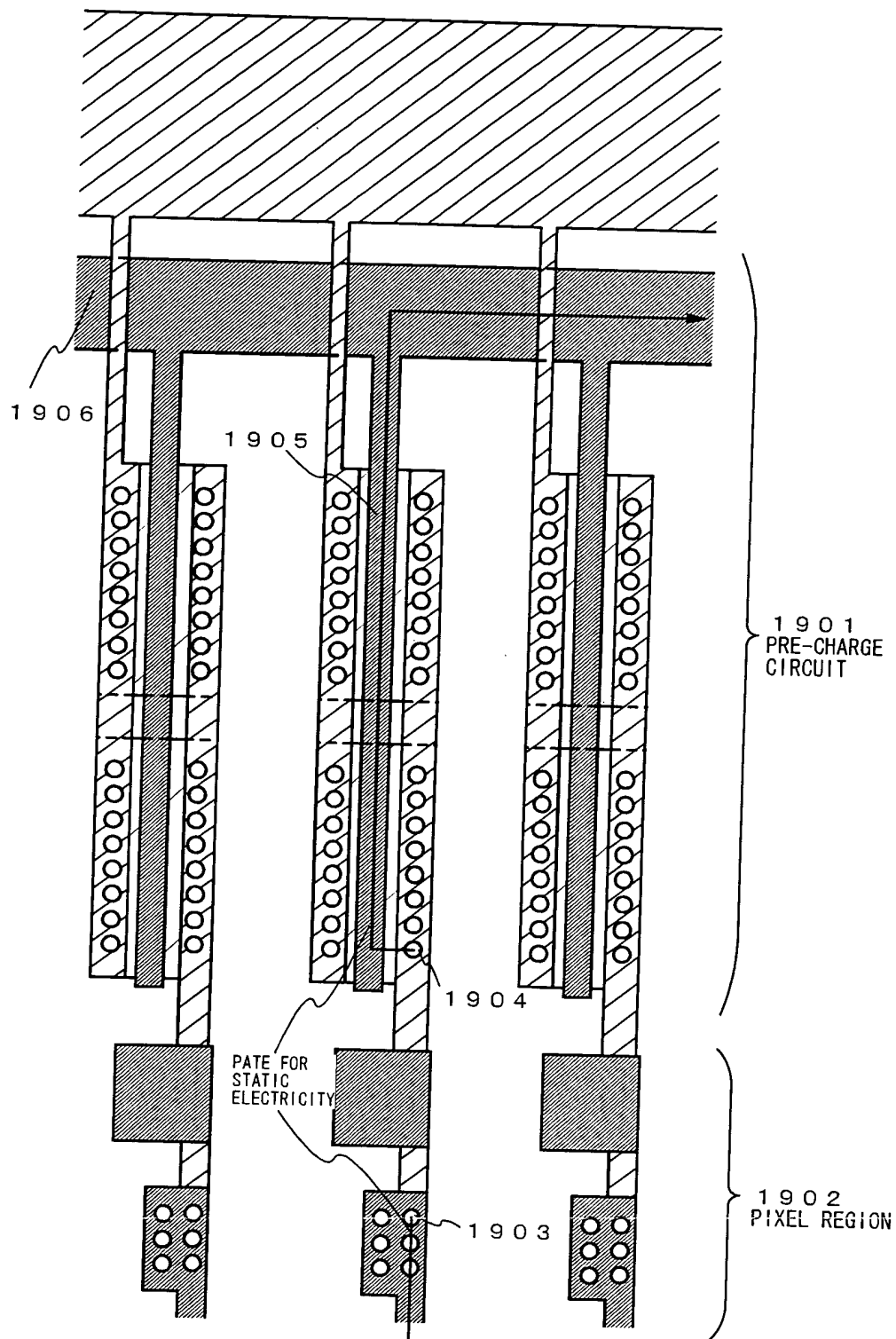


FIG. 20